



## COURSE DESCRIPTION CARD - SYLLABUS

Course name

Programmable digital circuits [N2EiT1>PUC2]

### Course

Field of study

Electronics and Telecommunications

Year/Semester

1/2

Area of study (specialization)

–

Profile of study

general academic

Level of study

second-cycle

Course offered in

Polish

Form of study

part-time

Requirements

compulsory

### Number of hours

Lecture

10

Laboratory classes

30

Other

0

Tutorials

0

Projects/seminars

0

### Number of credit points

6,00

### Coordinators

dr hab. inż. Olgierd Stankiewicz prof. PP  
olgierd.stankiewicz@put.poznan.pl

### Lecturers

dr hab. inż. Olgierd Stankiewicz prof. PP  
olgierd.stankiewicz@put.poznan.pl  
dr inż. Adam Grzelka  
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### Prerequisites

none

### Course objective

none

### Course-related learning outcomes

none

### Methods for verifying learning outcomes and assessment criteria

Learning outcomes presented above are verified as follows:

none

### Programme content

none

### Course topics

none

### Teaching methods

none

### Bibliography

none

### Breakdown of average student's workload

	Hours	ECTS
Total workload	210	10,00
Classes requiring direct contact with the teacher	75	3,00
Student's own work (literature studies, preparation for laboratory classes/ tutorials, preparation for tests/exam, project preparation)	135	7,00